

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Tews, <i>et al.</i>	Docket No.:	2004 P 51343 US
Serial No.:	TBD	Art Unit:	TBD
Filed:	HEREWITH	Examiner:	TBD
Title:	Semiconductor Structures and Manufacturing Methods		

Mail Stop Patent Application
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to examination on the merits, Applicants respectfully submit this Preliminary
Amendment as set forth below: